Click here for production status of specific part numbers.

MAXM15063 5V Output Evaluation Kit

Evaluates: MAXM15063 5V Output-Voltage Application

General Description

The MAXM15063 5V output evaluation kit (EV kit) provides a proven design to evaluate the MAXM15063 high-voltage, high-efficiency, synchronous step-down DC-DC module. The EV kit is programmed to deliver 5V output for loads up to 300mA. The EV kit features an adjustable input undervoltage lockout, selectable mode, and open-drain $\overline{RESET}$ signal. The MAXM15063 data sheet provides a complete description of the module that should be read in conjunction with this EV kit data sheet prior to modifying the demo circuit. For full module features, benefits and parameters, refer to the MAXM15063 data sheet.

Features

- Highly Integrated Solution
- Wide 12V to 60V Input Range
- Fixed 5V Output, Delivers Up To 300mA Output Current
- High 81.72% Efficiency (V_{IN} = 24V, V_{OUT} = 5V at 220mA)
- 500kHz Switching Frequency
- ENABLE/UVLO Input, Resistor-Programmable UVLO Threshold
- PFM Feature for Better Light-Load Efficiency
- Fixed Internal 4.1ms Soft-Start Time
- $\overline{RESET}$ Output, with Pullup Resistor to V_{CC}
- Overcurrent and Overtemperature Protection (OCP and OTP)
- Low-Profile, Surface-Mount Components
- Proven PCB Layout
- Fully Assembled and Tested
- Complies with CISPR22(EN55022) Class B Conducted and Radiated Emissions

Quick Start

Recommended Equipment

- One 4.5V to 60V DC, 300mA power supply
- 1.5W resistive load with 300mA sink capacity
- Four digital multimeters (DMM)
- MAXM15063EVKIT#

Equipment Setup and Test Procedure

The EV kit is fully assembled and tested. Follow the steps below to verify the board operation.

Caution: Do not turn on power supply until all connections are completed.

1) Set the power supply at a voltage between 4.5V and 60V. Then, disable the power supply.
2) Connect the positive terminal of the power supply to the VIN PCB pad and the negative terminal to the nearest GND PCB pad. Connect the positive terminal of the 300mA load to the VOUT PCB pad and the negative terminal to the nearest GND PCB pad.
3) Connect the DVM (DMM in voltage-measurement mode) across the VOUT PCB pad and the nearest GND PCB pad.
4) Verify that shunt is not installed on jumper J1 (see Table 1 for details).
5) Turn on the DC power supply.
6) Enable the load.
7) Verify that the DVM displays 5V.

***Ordering Information* appears at end of data sheet.**

319-100324; Rev 0; 3/19

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Detailed Description

The MAXM15063 EV kit is designed to demonstrate salient features of MAXM15063 power module. The EV kit includes an EN/UVLO PCB pad, and jumper J1, to enable the output at a desired input voltage. Jumper J2 allows selection of either PWM or PFM mode of operation based on light-load performance requirements. An additional $\overline{\text{RESET}}$ pad is available for monitoring if the converter output voltage is in regulation.

Output Capacitor Selection

X7R ceramic output capacitors are preferred due to their stability over temperature in industrial applications. The required output capacitor (C5) for 5V output is selected from Table 1 of the *MAXM15063* data sheet as 10µF/6.3V.

Fixed Output Voltage

The MAXM15063 is a fixed 5V output module. Connect FB of MAXM15063 directly to VOUT for feedback control.

Enable/Undervoltage-Lockout (EN/UVLO) Programming

The MAXM15063 offers an adjustable input undervoltage-lockout feature. In this EV kit, for normal operation, leave jumper J1 open. When J1 is left open, the MAXM15063 is enabled when the input voltage rises above 12V. To disable MAXM15063, install a jumper across pins 2-3 on J1. See Table 1 for J1 settings. A potential divider formed by R1 and R2 sets the input voltage (V_{INU}) at which the module is enabled. The value of resistor R1 is chosen to be 2.2MΩ, and R2 is calculated using the following equation:

$$R_2 = \frac{R_1 \times 1.215}{(V_{INU} - 1.215)}$$

where R1 and R2 are in kΩ,

For MAXM15063 to turn on at 12V input, the Resistor R2 is calculated to be 249kΩ.

Input Capacitor Selection

The input capacitor serves to reduce the current peaks drawn from the input power supply and reduces switching frequency ripple at the input. The input capacitance must be greater than or equal to the value given in Table 1 of *MAXM15063* data sheet. Input capacitor C3 is chosen to be 1µF/100V.

Electromagnetic Interference (EMI)

Compliance to conducted emissions (CE) standards requires an EMI filter at the input of a switching power converter. The EMI filter attenuates high-frequency cur¬rents drawn by the switching power converter, and limits the noise injected back into the input power source.

Use EMI filter components as shown in Figure 1 in conjunction with the schematic results in lower conducted emissions below CISPR22 Class B limits. The MAXM15063 EV kit PCB layout is also designed to limit radiated emissions from switching nodes of the power converter, resulting in radiated emissions below CISPR22 Class B limits.

Table 1. UVLO Enable/Disable Configuration (J1)

POSITION	EN/UVLO PIN	MAXM15063_ OUTPUT
Not Installed*	Connected to the center node of resistor-divider R1 and R2.	Programmed to startup at desired input-voltage level.
1-2	Connected to V_{IN}	Enabled if V_{IN} is greater than $V_{IN(MIN)}$.
2-3	Connected to GND	Disabled

**Default position*

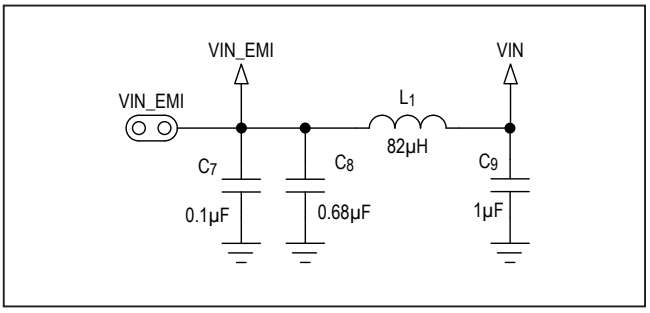


Figure 1. EMI Filter Components

Hot-Plug-In and Long Input Cables

The MAXM15063 EV kit PCB provides an optional electrolytic capacitor (C2, 4.7µF/100V) to dampen input voltage peaks and oscillations that can arise during hot-plug-in and/or due to long input cables. This capacitor limits the peak voltage at the input of the MAXM15063 power module, when the EV kit is powered directly from a precharged capacitive source or an industrial backplane PCB. Long input cables, between input power source and the EV kit circuit can cause input-voltage oscillations due to the inductance of the cables. The equivalent series resistance (ESR) of the electrolytic capacitor helps damp out the oscillations caused by long input cables. Further, capacitor C1 (0.1µF/100V), placed near the input of the board, helps in attenuating high frequency noise.

Mode of Operation

The MAXM15063 features PFM mode of operation to increase the efficiency at light-load condition. If the MODE pin is left unconnected during powerup, the module operates in PFM mode at light loads. If the MODE pin is connected to GND during power-up, the part operates in constant-frequency PWM mode at all loads. See Table 2 for J2 settings.

Internal LDO

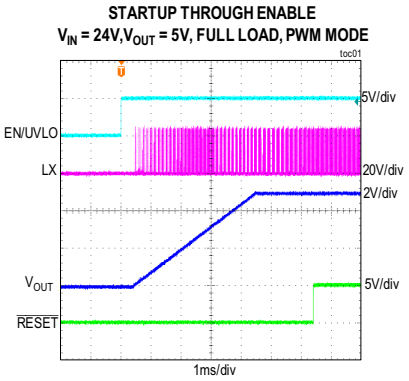
An internal regulator provides a 5V nominal supply to power the internal functions of the module. The output of the linear regulator (V_{CC}) should be bypassed with a 1µF capacitor C4 to GND.

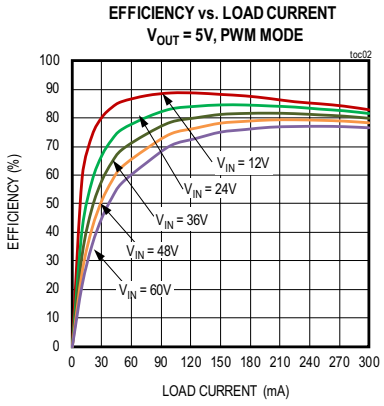
Table 2. Mode of Operation (J2)

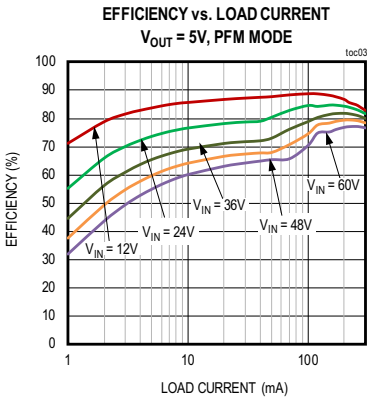
POSITION	MODE PIN
1-2	Operates in PWM mode.
Not Installed*	Operates in PFM mode at light-load conditions.

**Default position*

EV Kit Performance Report

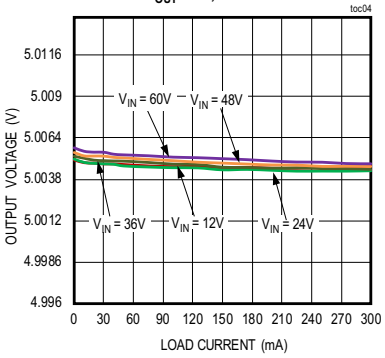




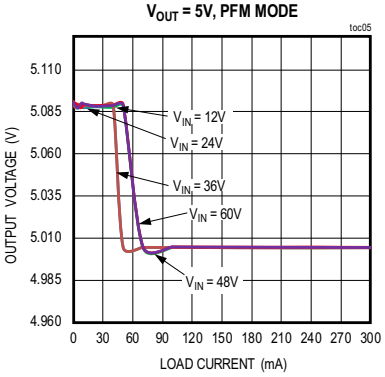


EV Kit Performance Report (continued)

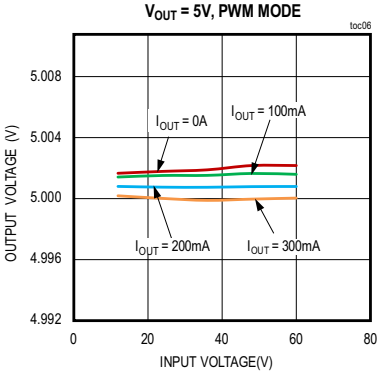
OUTPUT VOLTAGE vs. LOAD CURRENT
V_{OUT} = 5V, PWM MODE



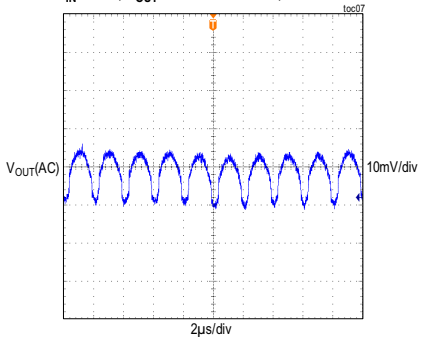
OUTPUT VOLTAGE vs. LOAD CURRENT
V_{OUT} = 5V, PFM MODE



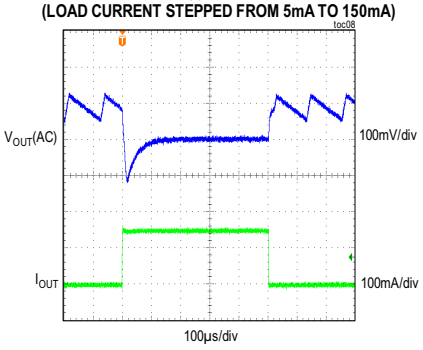
OUTPUT VOLTAGE vs. INPUT VOLTAGE
V_{OUT} = 5V, PWM MODE



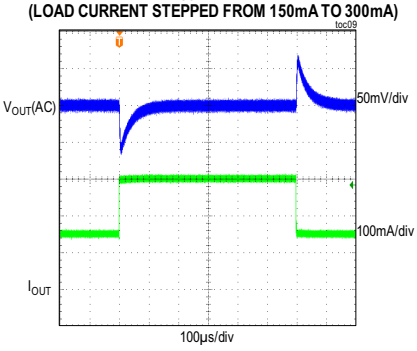
OUTPUT VOLTAGE RIPPLE
V_{IN} = 24V, V_{OUT} = 5V FULL LOAD, PWM MODE



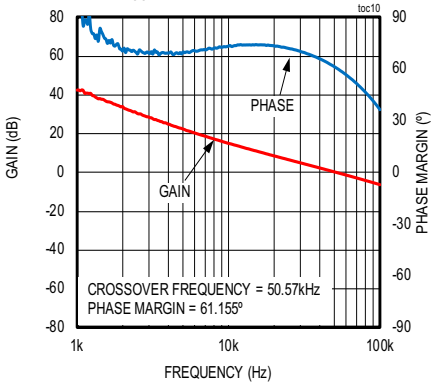
LOAD TRANSIENT RESPONSE
V_{IN} = 24V, V_{OUT} = 5V, PFM MODE
(LOAD CURRENT STEPPED FROM 5mA TO 150mA)



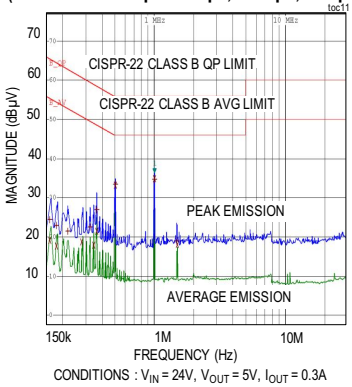
LOAD TRANSIENT RESPONSE
V_{IN} = 24V, V_{OUT} = 5V, PWM MODE
(LOAD CURRENT STEPPED FROM 150mA TO 300mA)



BODE PLOT
V_{IN} = 24V, V_{OUT} = 5V, FULL LOAD, PWM MODE

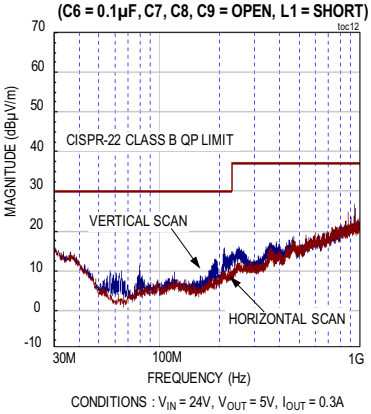


CONDUCTED EMISSION PLOT
(EMI FILTER C = 0.1µF + 0.68µF, L = 82µH, C = 1µF)



CONDITIONS : V_{IN} = 24V, V_{OUT} = 5V, I_{OUT} = 0.3A

RADIATED EMISSION PLOT
(C6 = 0.1µF, C7, C8, C9 = OPEN, L1 = SHORT)



CONDITIONS : V_{IN} = 24V, V_{OUT} = 5V, I_{OUT} = 0.3A

Ordering Information

PART	TYPE
MAXM15063EVKIT#	EV Kit

#Denotes RoHS compliant.

Component Suppliers

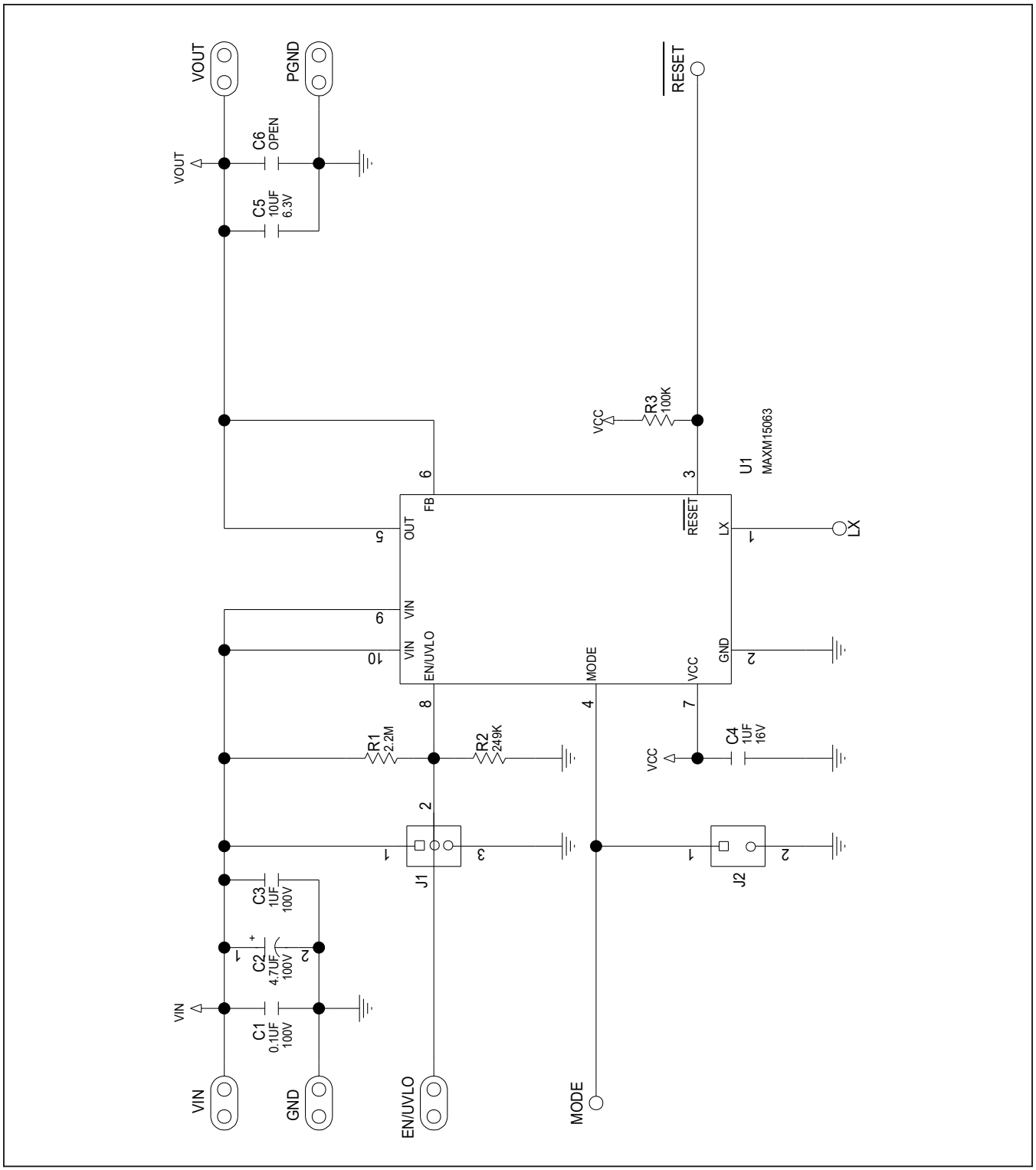
SUPPLIER	WEBSITE
Murata Americas	www.murata.com
NEC TOKIN America, Inc.	www.nec-tokinamerica.com
Panasonic Corp.	www.panasonic.com
SANYO Electric Co., Ltd.	www.sanyodevice.com
TDK Corp.	www.component.tdk.com
TOKO America, Inc.	www.tokoam.com

Note: *Indicate that you are using the MAXM15063 when contacting these component suppliers.*

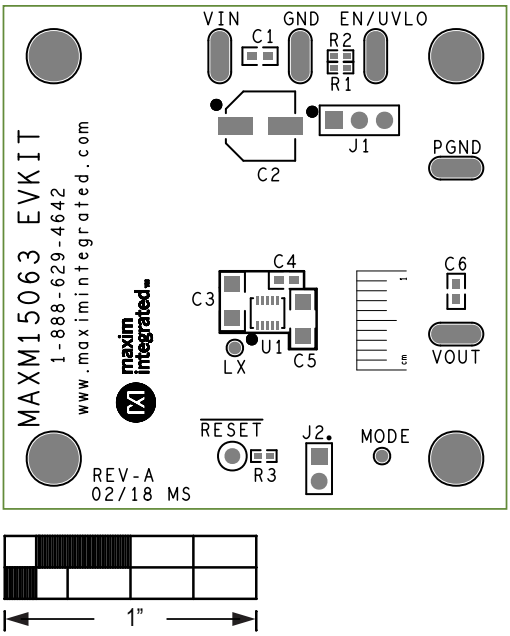
MAXM15063 5V EV Kit Bill of Materials

ITEM	QTY	DESIGNATION	DESCRIPTION	MANUFACTURER PARTNUMBER-1	MANUFACTURER PARTNUMBER-2
1	1	C1	0.1µF±10%,100V, X7R ceramic capacitor (0603)	YAGEO PHICOMP CC0603KRX7R0BB104	
2	1	C2	4.7µF±20%,100V, Aluminimum Capacitor	NICHICON UUR2A4R7MCL6GS	
3	1	C3	1µF±10%,100V, X7R ceramic capacitor (1206)	MURATA GRM31CR72A105KA01	TDK C3216X7R2A105K160AA
4	1	C4	1µF±10%,16V, X7R ceramic capacitor (0603)	MURATA GRM188R71C105KA12	TDK C1608X7R1C105K080AC
5	1	C5	10µF±10%,6.3V, X7R ceramic capacitor (1206)	MURATA GRM31CR70J106K	
6	1	C6	OPEN (OPTIONAL : 0.1µF±10%,50V, X7R ceramic capacitor (0603))	Murata GRM188R71H104KA93	
7	1	R1	2.2MΩ ±1% resistor (0402)	VISHAY DALE CRCW04022M20FK	
8	1	R2	249kΩ ±1% resistor (0402)	VISHAY DALE CRCW0402249KFK	
9	1	R3	100kΩ ±1% resistor (0402)	VISHAY DALE CRCW0402100KFK	YAGEO PHICOMP RC0402FR-07100KL
10	1	U1	MAXM15063, 10-pin micro-SLIC Power Module	MAXIM MAXM15063AMB+T	
11	1	L1	OPTIONAL : 82µH Shielded Wirewound Inductor(2016)	Murata LQH2MPN820MGRL	
12	1	C7	OPTIONAL : 0.1µF±10%,100V, X7R ceramic capacitor (0603)	Murata GRM188R72A104KA35	
13	1	C8	OPTIONAL : 0.68µF±10%,100V, X7R ceramic capacitor (1206)	Murata GRM31MR72A684KA35	
14	1	C9	OPTIONAL : 1µF±10%100V, X7R ceramic capacitor (1206)	Murata GRM31CR72A105KA01L	

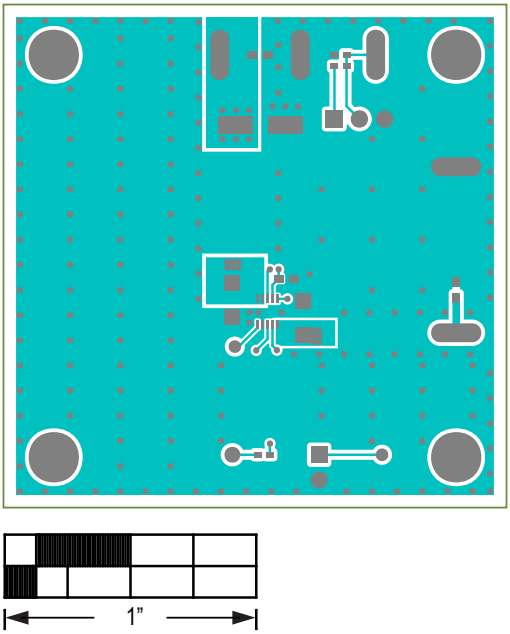
MAXM15063 5V EV Kit Schematic



MAXM15063 5V EV Kit PCB Layout Diagrams

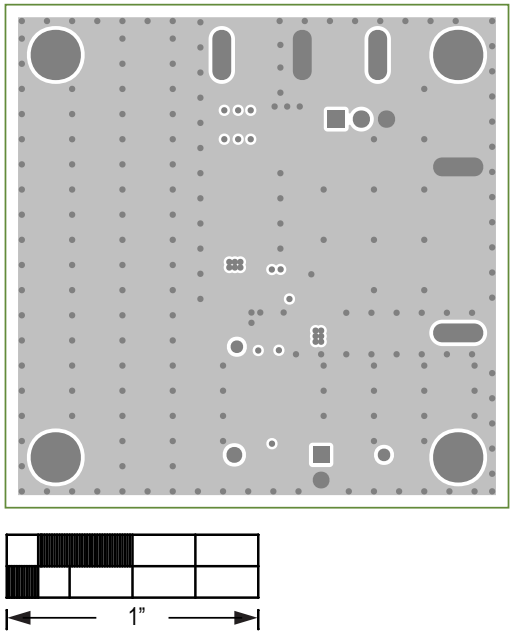


MAXM15063 EV Kit PCB Layout—Silk Top

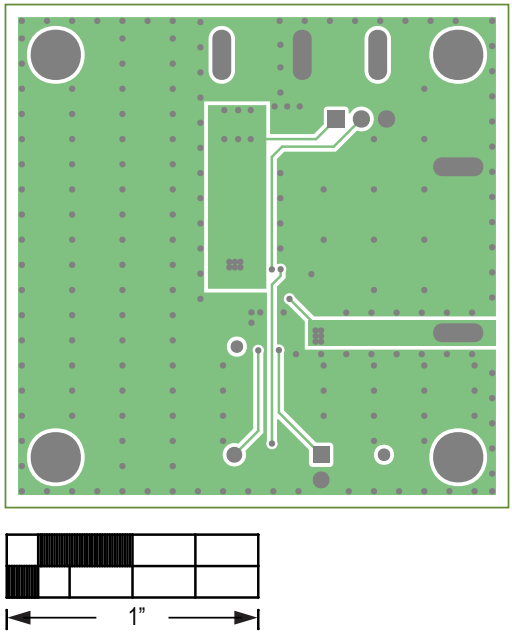


MAXM15063 EV Kit PCB Layout—Top Layer

MAXM15063 5V EV Kit PCB Layout Diagrams (continued)

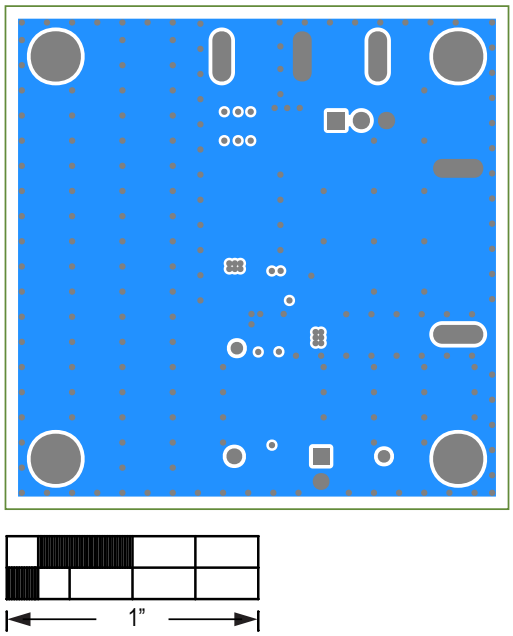


MAXM15063 EV Kit PCB Layout—Layer 2 Ground



MAXM15063 EV Kit PCB Layout—Layer 3 Power

MAXM15063 5V EV Kit PCB Layout Diagrams (continued)



MAXM15063 EV Kit PCB Layout—Bottom Layer

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	3/19	Initial release	—

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